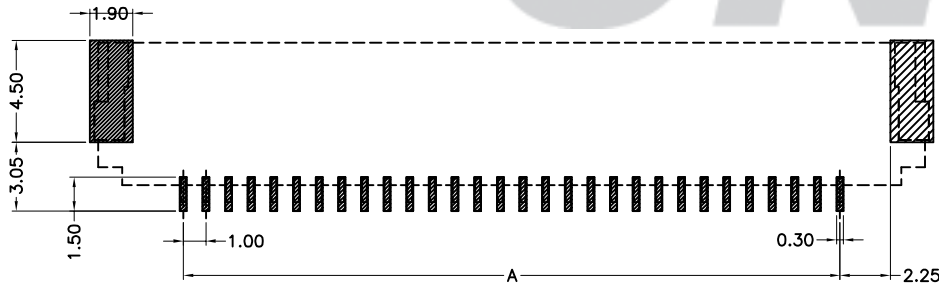
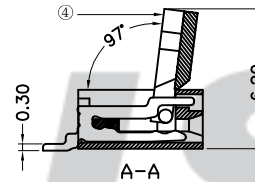
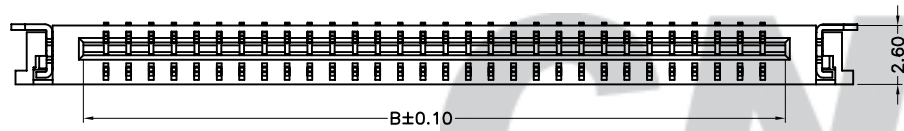
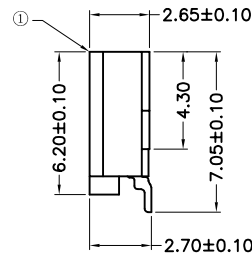
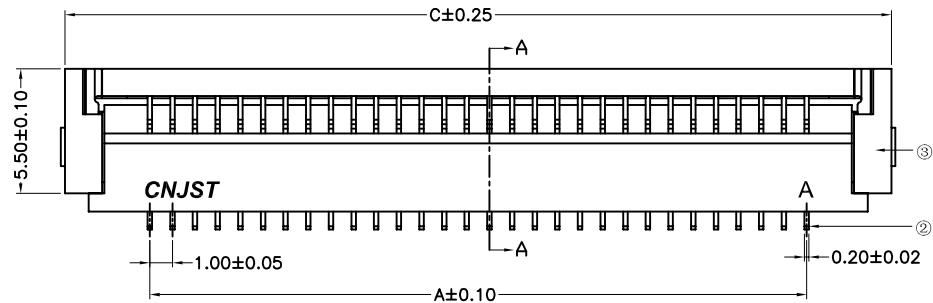
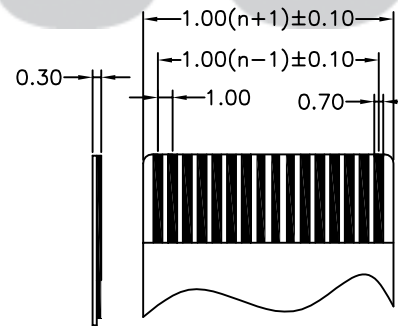


RoHS Compliant

成品图



SUGGESTED PCB LAYOUT
(COMPONENT SIDE)



Accommodated FFC/FPC

SPECIFICATION

- 1 Contact resistance: $\leq 30m\Omega$
- 2 Insulation resistance: $\geq 500M\Omega$
- 3 Rated voltage: 50 V AC DC
- 4 Rated current: 0.5 A AC DC
- 5 Withstand Voltage: 500V AC/minute
- 6 Temperature Range: $-25^{\circ}C \sim +85^{\circ}C$

ORDER INFORMATION:

F1005AWR--F2MG-R**

Part No. _____
 No. of Circuits: _____
 2~30
 PLASTIC MATERIAL: _____
 F2=LCP (Locker:LCP)

PACKAGING:
 R=Reel
 PLATING:
 G=Matte Sn
 +Gold

Circuits	Dimensions (mm)			Circuits	Dimensions (mm)		
	A	B	C		A	B	C
02	1.00	3.10	8.50	17	16.00	18.10	23.50
03	2.00	4.10	9.50	18	17.00	19.10	24.50
04	3.00	5.10	10.50	19	18.00	20.10	25.50
05	4.00	6.10	11.50	20	19.00	21.10	26.50
06	5.00	7.10	12.50	21	20.00	22.10	27.50
07	6.00	8.10	13.50	22	21.00	23.10	28.50
08	7.00	9.10	14.50	23	22.00	24.10	29.50
09	8.00	10.10	15.50	24	23.00	25.10	30.10
10	9.00	11.10	16.50	25	24.00	26.10	31.50
11	10.00	12.10	17.50	26	25.00	27.10	32.50
12	11.00	13.10	18.50	27	26.00	28.10	33.50
13	12.00	14.10	19.50	28	27.00	29.10	34.50
14	13.00	15.10	20.50	29	28.00	30.10	35.50
15	14.00	16.10	21.50	30	29.00	31.10	36.50
16	15.00	17.10	22.50				

REV.	DATE	DESCRIPTION	APPD.
B/0	2016.04.12	NEW RELEASE	

④	Locker	1	LCP (UL94V-0)	Nature (Black)
③	Solder Tab	2	Phosphor Bronze	Mattn Sn
②	Contacts	02~30	Phosphor Bronze	Gold-plated
①	Insulator	1	LCP (UL94V-0)	Nature
NO.	DESCRIPTION	QTY	MATERIAL	PLATING/COLOR

Tolerance
 X.X ±0.30
 X.XX ±0.20
 X.XXX ±0.10
 Angle ±3.0°

UNIT: mm

Model Name: 1.00mm SMT ZIF Upside FFC/FPC Connectors
 Model No: F1005AWR-**-F2MG-R

SCALE: FULL REVISION: B/0

CNJST 浙江捷仕泰电子有限公司
 ZHEJIANG JIESHITAI ELECTRONIC CO., LTD

Drawn: 张育斌 2016.06.11
 Checked: 林旭芝
 Approved: 刘祚杰
 DWG NO. CNJST/GCCP-0231