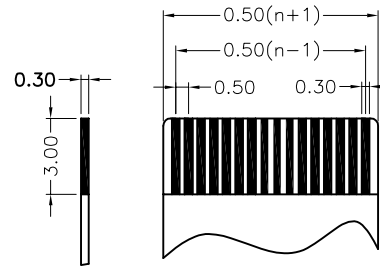
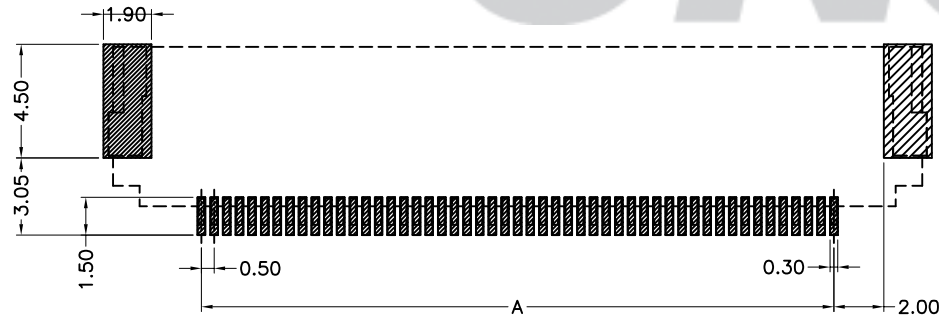
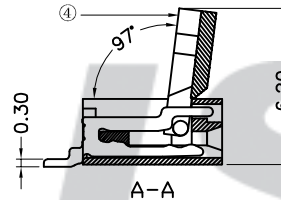
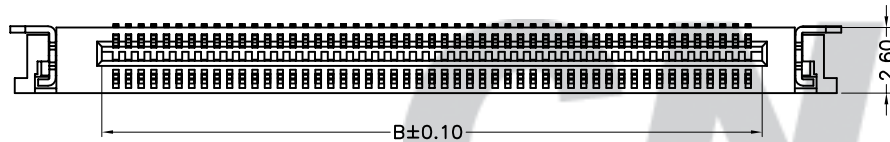
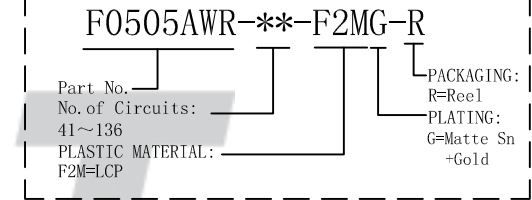


SPECIFICATION

- 1 Contact resistance: $\leq 30m\Omega$
- 2 Insulation resistance: $\geq 500M\Omega$
- 3 Rated voltage: 50 V AC DC
- 4 Rated current: 0.5 A AC DC
- 5 Withstand Voltage: 500V AC/minute
- 6 Temperature Range: $-25^{\circ}C \sim +85^{\circ}C$

ORDER INFORMATION:



Accommodated FFC/FPC

Circuits	Dimensions (mm)		
	A	B	C
41	20.00	21.10	27.00
51	25.00	26.10	32.00
55	27.00	28.10	34.00
60	29.50	30.60	39.00
84	41.50	42.60	61.00
100	49.50	50.60	69.00
128	63.50	64.60	83.00
136	67.50	68.60	87.00

SUGGESTED PCB LAYOUT
(COMPONENT SIDE)

NO.	DESCRIPTION	QTY	MATERIAL	PLATING/COLOR
④	Locker	1	LCP (UL94V-0)	Nature (Black)
③	Solder Tab	2	Phosphor Bronze	Mattn Sn
②	Contacts	40~136	Phosphor Bronze	Gold-plated
①	Insulator	1	LCP (UL94V-0)	Nature

Tolerance	Model Name: 0.50mm SMT ZIP Upside FFC/FPC Connectors
X.X ±0.30	Model No: F0505AWR-**-F2MG-R
X.XX ±0.20	
X.XXX ±0.10	
Angle ±3.0°	
UNIT: mm	



SCALE: FULL

REVISION: B/0

CNJST 浙江捷仕泰电子有限公司
ZHEJIANG JIESHITAI ELECTRONIC CO., LTD

Drawn: 张育斌 2016.06.11

Checked: 林旭芝

Approved: 刘祚杰

DWG NO. CNJST/GCCP-0230

REV.	DATE	DESCRIPTION	APPD.
B/0	2016.04.12	NEW RELEASE	